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**BOX AF**

RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2811

AF/2800  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

O I P E  
JAN 14 2002  
PATENT & TRADEMARK OFFICE

In re Application of

Atty. Docket

original copy  
#7

HAVEED MAJID ET AL

PHA 23,843

Serial No. 09\440,595

Group Art Unit: 2811

Filed: November 15, 1999

Examiner: N. Parekh

Title: MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN  
POWER APPLICATIONS

Commissioner for Patents  
Washington, D.C. 20231

RECEIVED  
JAN 16 2002  
FC 2800 MAIL ROOM

NOTICE OF APPEAL

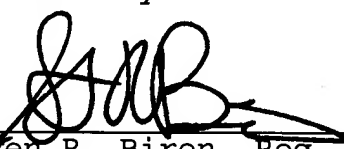
Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated July 24, 2001 of the Examiner finally rejecting claims 1-7.

- [ X ] Please charge the fee of \$320.00 to Deposit Account No. 14-1270.
- [ ] No additional fee is required, because the fee was paid in a prior appeal.

Respectfully submitted,

01/15/2002 CNGUYEN 00000002 141270 09440595  
01 FC:119 320.00 CH

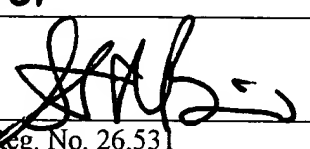
By   
Steven R. Biren, Reg. No. 26,531  
Attorney  
(914) 333-9630

CERTIFICATE OF MAILING

It is hereby certified that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to:

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